REMARKS

Claims 1-5 are pending in the application. Claims 1 has been amended, leaving claims 1 and 5 for consideration upon entry of the present Amendment. Support for the amendment to claim 1 is found in Figure 1.

Claims 1 and 2 stand rejected under 35 U.S.C. §103(a) as being unpatentable over prior art figure 3 in view of Osada et al. (US 5,973,456) ("Osada"). For an obviousness rejection to be proper, the Examiner must meet the burden of establishing that all elements of the invention are disclosed in the prior art. *In re Fine*, 5 U.S.P.Q.2d 1596, 1598 (Fed. Cir. 1988); *In Re Wilson*, 165 U.S.P.Q. 494, 496 (C.C.P.A. 1970); *Amgen v. Chugai Pharmaceuticals Co.*, 927 U.S.P.Q.2d, 1016, 1023 (Fed. Cir. 1996). In this case, the references do not teach or suggest all of the limitations.

Claim 1, as amended, includes the following limitation: "each of said gate signal lines is connected to said gate drive circuits at both ends of said gate signal lines." Neither prior art figure 3 nor Osada teach or suggest that limitation. While Osada teaches drive circuits on both sides, only one of the left or right drive circuit is connected to the gate line, alternatively for each row.

Thus, claim 1 is patentable over prior art figure 3 and Osada. Claim 2 includes all of the limitations as claim 1, and thus, claim 2 is also patentable over prior art figure 3 and Osada. Accordingly, Applicants respectfully request that the rejection as to claims 1 and 2 under 35 U.S.C. § 103(a) be withdrawn.

Claims 3-5 stand rejected under 35 U.S.C. §103(a) as being unpatentable over prior art figure 3 in view of Peng et al. (US 6,078,142) ("Peng"). For an obviousness rejection to be proper, the Examiner must meet the burden of establishing that all elements of the invention are disclosed in the prior art. *In re Fine*, 5 U.S.P.Q.2d 1596, 1598 (Fed. Cir. 1988); *In Re Wilson*, 165 U.S.P.Q. 494, 496 (C.C.P.A. 1970); *Amgen v. Chugai Pharmaceuticals Co.*, 927 U.S.P.Q.2d, 1016, 1023 (Fed. Cir. 1996). In this case, the references do not teach or suggest all of the limitations.

Claims 3-5 include all of the limitations of claim 1. As explained above, prior art figure 3 and Osada do not teach or suggest all of the limitations of claim 1. Moreover, Peng does not cure the deficiencies of Osada because in Peng there is no teaching or suggestion of "each of said gate signal lines is connected to said gate drive circuits at both ends of said gate signal lines." Accordingly, Applicants respectfully request that the rejection as to claims 3-5 under 35 U.S.C. § 103(a) be withdrawn.

In addition, attached hereto is a marked-up version of the changes made to the application. The attached page is captioned "**Version with Markings to Show Changes Made.**"

In view of the foregoing, it is respectfully submitted that the instant application is in condition for allowance. Accordingly, it is respectfully requested that this application be allowed and a Notice of Allowance issued. If the Examiner believes that a telephone conference with Applicants' attorneys would be advantageous to the disposition of this case, the Examiner is cordially requested to telephone the undersigned.

In the event the Commissioner of Patents and Trademarks deems additional fees to be due in connection with this application, Applicants' attorney hereby authorizes that such fee be charged to Deposit Account No. 06-1130.

Respectfully submitted,

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Version with Markings to Show Changes Made

IN THE CLAIMS:

Please amend claim 1 in "marked up" format, as follows:

1. (Marked up/Amended) An active matrix type electroluminescence display device comprising:

a plurality of display pixels arranged in rows and columns in a matrix form;

gate signal lines, each of which is connected to and shared by a plurality of display pixels provided on each row; and

gate drive circuits for sequentially supplying select signals to said gate signal lines; wherein

each of said display pixels includes an electroluminescence element, a first thin film transistor in which a display signal is applied to the drain and which is switched on and off in response to said select signal, and a second thin film transistor for driving said electroluminescence element based on said display signal; and

said gate drive circuits are placed so that said select signals are supplied from both ends of said gate signal lines to said gate signal lines, each of said gate signal lines is connected to said gate drive circuits at both ends of said gate signal lines.